



TO-92

123



SOT-89



Pin Definition:

Reference
Anode
Cathode



SOT-23

Pin Definition:

1. Reference 2. Cathode

3. Anode

SOP-8

Pin Definition:

Cathode
Reference
Anode
Anode

3. Anode 6. Anode

4. N/C 5. N/C

General Description

TS431 series integrated circuits are three-terminal programmable shunt regulator diodes. These monolithic IC voltage references operate as a low temperature coefficient zener which is programmable from V_{REF} to 36 volts with two external resistors. These devices exhibit a wide operating current range of 1.0 to 100mA with a typical dynamic impedance of 0.22Ω . The characteristics of these references make them excellent replacements for zener diodes in many applications such as digital voltmeters, power supplies, and op amp circuitry. The 2.5V reference makes it convenient to obtain a stable reference from 5.0V logic supplies, and since The TS431 series operates as a shunt regulator, it can be used as either a positive or negative stage reference.

Features

 Precision Reference Voltage TS431 – 2.495V ±2% TS431A – 2.495V ±1%

TS431B - 2.495V ±1%

- Equivalent Full Range Temp. Coefficient: 50ppm/ °C
- Programmable Output Voltage up to 36V
- Fast Turn-On Response
- Sink Current Capability of 1~100mA
- Low Dynamic Output Impedance: 0.2Ω
- Low Output Noise

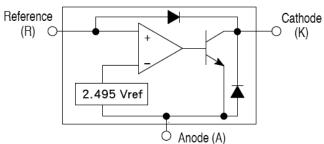
Ordering Information

Part No.	Package	Packing
TS431 <u>x</u> CT B0	TO-92	1Kpcs / Bulk
TS431 <u>x</u> CT B0G	TO-92	1Kpcs / Bulk
TS431 <u>x</u> CT A3	TO-92	2Kpcs / Ammo
TS431 <u>x</u> CT A3G	TO-92	2Kpcs / Ammo
TS431 <u>x</u> CX RF	SOT-23	3Kpcs / 7" Reel
TS431 <u>x</u> CX RFG	SOT-23	3Kpcs / 7" Reel
TS431xCY RM	SOT-89	1Kpcs / 7" Reel
TS431xCY RMG	SOT-89	1Kpcs / 7" Reel
TS431 <u>x</u> CS RL	SOP-8	2.5Kpcs / 13" Reel
TS431xCS RLG	SOP-8	2.5Kpcs / 13" Reel

Note: Where <u>xx</u> denotes voltage tolerance Blank: ±2%, A: ±1%, B: ±0.5%

"G" denotes for Halogen free products

Block Diagram



Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter		Symbol	Limit	Unit
Cathode Voltage (Note 1)		V_{KA}	37	V
Continuous Cathode Current R	ange	I _K	-100 ~ +150	mA
Reference Input Current Range)	I _{REF}	-0.05 ~ +10	mA
	TO-92		0.625	
Power Dissipation	SOT-23	P_{D}	0.30	W
	SOT-89 / SOP-8		0.50	
Junction Temperature		TJ	+150	°C
Operating Temperature Range		T _{OPER}	0 ~ +70	°C
Storage Temperature Range		T _{STG}	-65 ~ +150	°C

Note 1: Voltage values are with respect to the anode terminal unless otherwise noted.





Recommend Operating Condition

Parameter	Symbol	Limit	Unit
Cathode Voltage (Note 1)	V_{KA}	Ref ~ 36	V
Continuous Cathode Current Range	I _K	1 ~ 100	mA

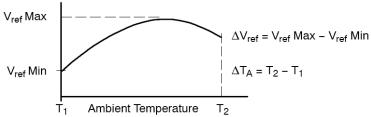
Recommend Operating Condition

Parameter Sy		Symbol	Test Conditions	Min	Тур	Max	Unit
	TS431		V _{KA} =V _{REF} , I _K =10mA (Figure 1)	2.446		2.550	
Reference voltage	TS431A	V_{REF}	Ta=25°C	2.470	2.495	2.520	V
	TS431B		1a-25 C	2.483		2.507	7
Deviation of reference voltage	input	ΔV_{REF}	$V_{KA} = V_{REF}$, $I_K = 10$ mA (Figure 1) Ta= full range		3	17	mV
Radio of change in Vr	ef to	ΔV_REF	I_{KA} =10mA, V_{KA} = 10V to V_{REF}		-1.4	-2.7	mV/V
change in cathode Vo	ltage	/ΔV _{KA}	V _{KA} = 36V to 10V (Figure 2)		-1.0	-2.0	IIIV/V
Reference Input curre	nt	I _{REF}	R1=10KΩ, R2= ∞ , I _{KA} =10mA Ta= full range (Figure 2)		0.7	4.0	uA
Deviation of reference input current, over temp.		ΔI_{REF}	R1=10KΩ, R2= ∞ , I _{KA} =10mA Ta= full range (Figure 2)		0.4	1.2	uA
			V_{REF} =0V (Figure 3), V_{KA} =36V			1.0	uA
Off-state Cathode Cur	rrent	I _{KA} (off)	V _{REF} =0V (Figure 3), V _{KA} =36V T _J =-25°C~125°C (Value is defined by design)		1	30	uA
Dynamic Cliffilit impedance Z _{1/2}		f <1KHz, $V_{KA} = V_{REF}$ I_{KA} =1mA to 100mA (Figure 1)		0.22	0.5	Ω	
Minimum operating cathode current I_{KA} (min) $V_{KA} = V_{REF}$ (Figure 1)			0.4	0.6	mA		

^{*} The deviation parameters ΔV_{REF} and ΔI_{REF} are defined as difference between the maximum value and minimum value obtained over the full operating ambient temperature range that applied.

* The average temperature coefficient of the reference input voltage, αV_{REF} is defined as:

$$\alpha V_{ref} \left(\frac{ppm}{^{\circ}C} \right) = \frac{\left(\frac{(\Delta V_{ref})}{V_{ref} (T_{A} = 25^{\circ}C)} \times 10^{6} \right)}{\Delta T_{A}}$$



Where: **T2-T1** = full temperature change.

 αV_{REF} can be positive or negative depending on whether the slope is positive or negative. Example: Maximum V_{REF} =2.496V at 30°C, minimum V_{REF} =2.492V at 0°C, V_{REF} =2.495V at 25°C, ΔT =70°C

$$\alpha V_{REF}$$
 | = [4mV / 2495mV] * 10⁶ / 70°C ≈ 23ppm/°C

Because minimum V_{REF} occurs at the lower temperature, the coefficient is positive.

* The dynamic impedance ZKA is defined as:

$$|Z_{K\Delta}| = \Delta V_{K\Delta} / \Delta I_{K\Delta}$$

* When the device operating with two external resistors, R1 and R2, (refer to Figure 2) the total dynamic impedance of the circuit is given by:

$$|Z_{KA}| = \Delta v / \Delta i | \approx Z_{KA} | * (1 + R1 / R2)$$





Test Circuits

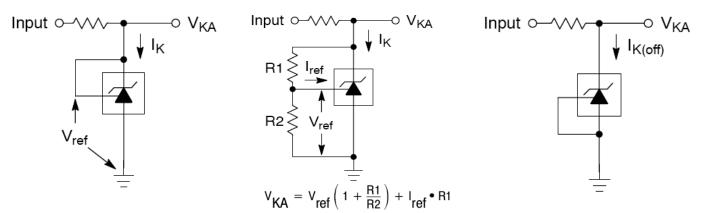


Figure 1: $V_{KA} = V_{REF}$

Figure 2: $V_{KA} > V_{REF}$

Figure 3: Off-State Current

Additional Information – Stability

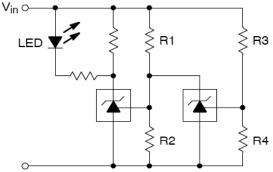
When The TS431/431A/431B is used as a shunt regulator, there are two options for selection of C_L , are recommended for optional stability:

- A) No load capacitance across the device, decouple at the load.
- B) Large capacitance across the device, optional decoupling at the load.

The reason for this is that TS431/431A/431B exhibits instability with capacitances in the range of 10nF to 1uF (approx.) at light cathode current up to 3mA (typ). The device is less stable the lower the cathode voltage has been set for. Therefore while the device will be perfectly stable operating at a cathode current of 10mA (approx.) with a 0.1uF capacitor across it, it will oscillate transiently during start up as the cathode current passes through the instability region. Select a very low capacitance, or alternatively a high capacitance (10uF) will avoid this issue altogether. Since the user will probably wish to have local decoupling at the load anyway, the most cost effective method is to use no capacitance at all directly across the device. PCB trace/via resistance and inductance prevent the local load decoupling from causing the oscillation during the transient start up phase.

Note: if the TS431/431A/431B is located right at the load, so the load decoupling capacitor is directly across it, then this capacitor will have to be ≤ 1 nF or ≥ 10 uF.

Applications Examples



L.E.D. indicator is 'ON' when V_{in} is between the upper and lower limits,

Lower limit =
$$\left(1 + \frac{R1}{R2}\right) V_{ref}$$

Upper limit = $\left(1 + \frac{R3}{R4}\right) V_{ref}$

Figure 4: Voltage Monitor

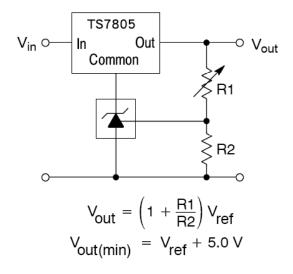


Figure 5: Output Control for Three Terminal Fixed Regulator





Applications Examples (Continue)

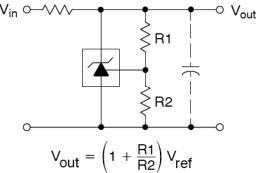


Figure 6: Shunt Regulator

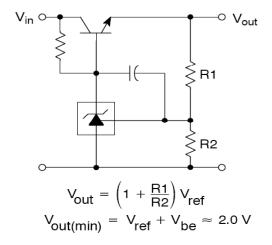


Figure 8: Series Pass Regulator

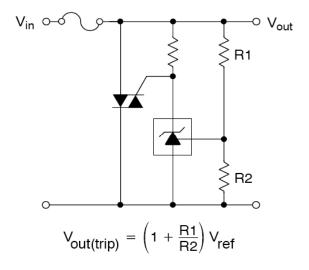


Figure 10: TRIAC Crowbar

Adjustable Precision Shunt Regulator

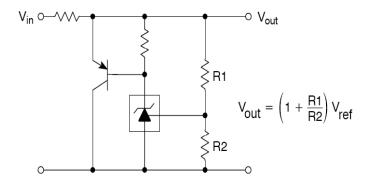


Figure 7: High Current Shunt Regulator

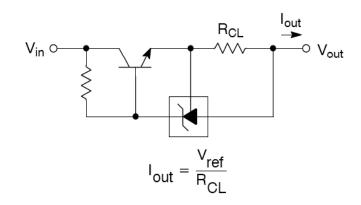


Figure 9: Constant Current Source

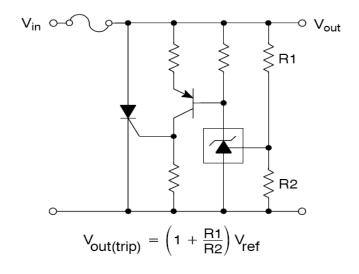
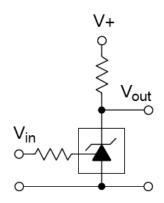


Figure 11: SCR Crowbar





Applications Examples (Continue)



Vin	Vout
<vref< td=""><td>V+</td></vref<>	V+
>Vref	≈0.74V

Figure 12: Single-Supply Comparator with Temperature-Compensated Threshold

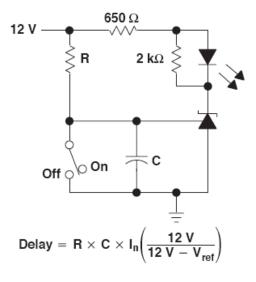


Figure 14: Delay Timer

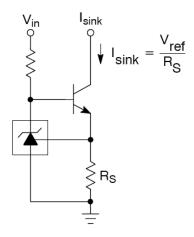


Figure 13: Constant Current Sink





Typical Performance Characteristics

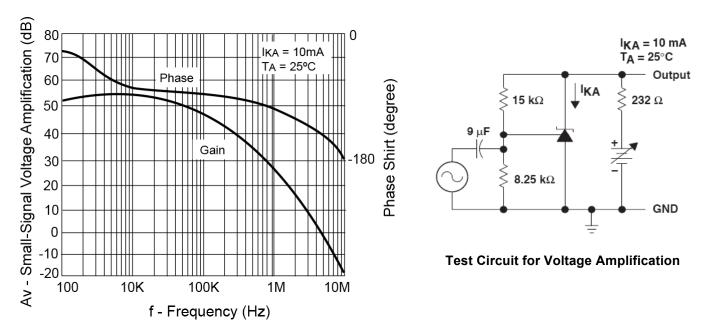


Figure 14: Small-Signal Voltage Gain and Phase Shirt vs. Frequency

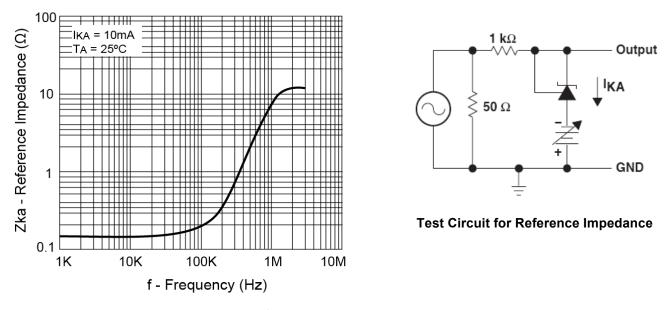
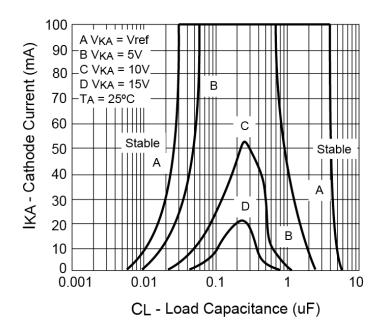


Figure 15: Reference Impedance vs. Frequency

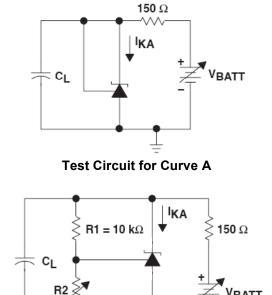




Typical Performance Characteristics (Continue)

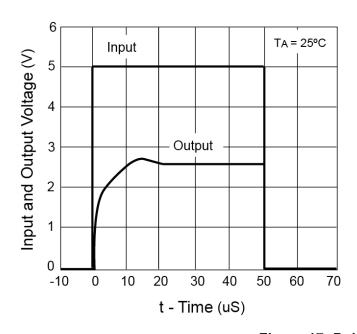


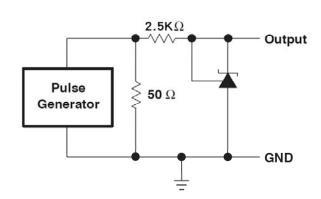
The areas under the curves represent conditions that may cause the device to oscillate. For curves B, C, and D, R2 and V+ were adjusted to establish the initial VKA and IKA conditions with CL=0. VBATT and CL then were adjusted to determine the ranges of stability.



Test Circuit for Curve B, C and D

Figure 16: Stability Boundary Condition





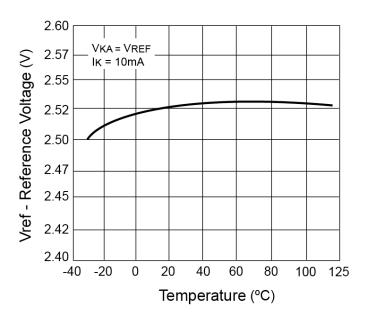
Test Circuit for Pulse Response, lk=1mA

Figure 17: Pulse Response





Electrical Characteristics



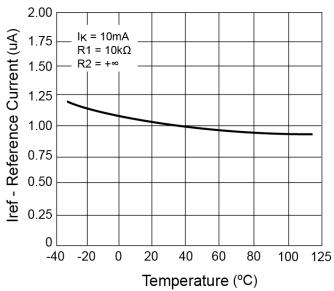


Figure 18: Reference Voltage vs. Temperature

Figure 19: Reference Current vs. Temperature

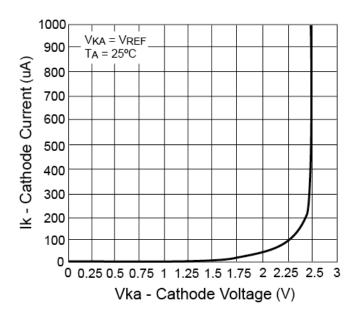
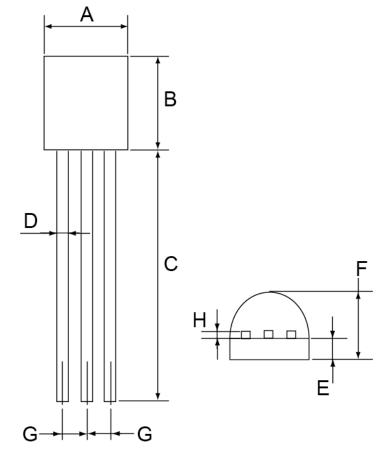


Figure 20: Cathode Current vs. Cathode Voltage



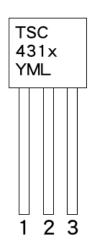


TO-92 Mechanical Drawing



TO-92 DIMENSION				
DIM	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.30	4.70	0.169	0.185
В	4.30	4.70	0.169	0.185
C	13.53 (typ)		0.532 (typ)	
D	0.39	0.49	0.015	0.019
Е	1.18	1.28	0.046	0.050
F	3.30	3.70	0.130	0.146
G	1.27	1.31	0.050	0.051
Н	0.33	0.43	0.013	0.017

Marking Diagram



X = Tolerance Code

 $(A = \pm 1\%, B = \pm 0.5\%, C = \pm 0.25\%, Blank = \pm 2\%,)$

Y = Year Code

M = Month Code

(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apl, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)

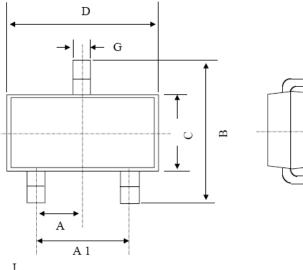
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L = Lot Code

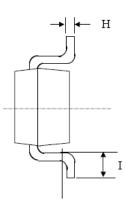




SOT-23 Mechanical Drawing

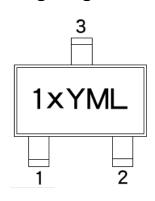


F



SOT-23 DIMENSION					
DIM	MILLIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX.	
Α	0.95	BSC	0.037 BSC		
A1	1.9 I	BSC	0.074	BSC	
В	2.60	3.00	0.102	0.118	
С	1.40	1.70	0.055	0.067	
D	2.80	3.10	0.110	0.122	
Е	1.00	1.30	0.039	0.051	
F	0.00	0.10	0.000	0.004	
G	0.35	0.50	0.014	0.020	
Н	0.10	0.20	0.004	0.008	
I	0.30	0.60	0.012	0.024	
J	5°	10°	5°	10°	





1 = Device Code

X = Tolerance Code

 $(A = \pm 1\%, B = \pm 0.5\%, C = \pm 0.25\%, Blank = \pm 2\%,)$

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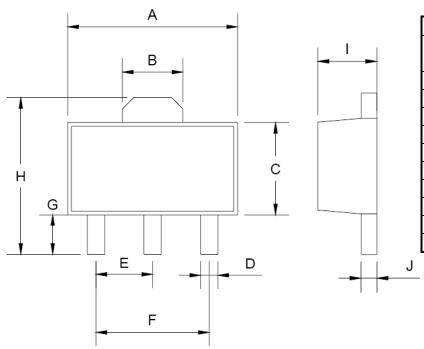
X=Oct, Y=Nov, Z=Dec)

L = Lot Code



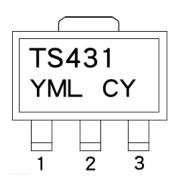


SOT-89 Mechanical Drawing



SOT-89 DIMENSION					
DIM	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	4.40	4.60	0.173	0.181	
В	1.50	1.7	0.059	0.070	
С	2.30	2.60	0.090	0.102	
D	0.40	0.52	0.016	0.020	
Е	1.50	1.50	0.059	0.059	
F	3.00	3.00	0.118	0.118	
G	0.89	1.20	0.035	0.047	
Н	4.05	4.25	0.159	0.167	
	1.4	1.6	0.055	0.068	
J	0.35	0.44	0.014	0.017	

Marking Diagram



Y = Year Code

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(**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apl, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)

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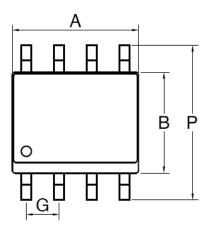
L = Lot Code

CY = Package Code

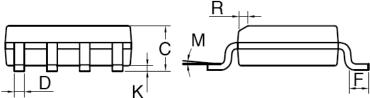




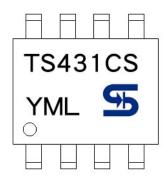
SOP-8 Mechanical Drawing



SOP-8 DIMENSION					
DIM	MILLIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX.	
Α	4.80	5.00	0.189	0.196	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.054	0.068	
D	0.35	0.49	0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27BSC		0.05	BSC	
K	0.10	0.25	0.004	0.009	
M	0°	7°	0°	7°	
Р	5.80	6.20	0.229	0.244	
R	0.25	0.50	0.010	0.019	



Marking Diagram



Y = Year Code

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