Advance Information

Surface Mount Schottky Power Rectifier

SMB Power Surface Mount Package

... employing the Schottky Barrier principle in a metal-to-silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes.

- Compact Package with J-Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction
- Guardring for Over–Voltage Protection
- Low Forward Voltage Drop
- Mechanical Characteristics:
- Case: Molded Epoxy
- Epoxy Meets UL94, VO at 1/8"
- Weight: 95 mg (approximately)
- Polarity: Notch in Plastic Body Indicates Cathode Lead
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Available in 12 mm Tape, 2500 Units per 13" Reel, Add "T3" Suffix to Part Number
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Marking: B14L

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	Volts	
Average Rectified Forward Current (At Rated V _R , T _C = 110°C)	lo	1.0	Amps	
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 100 kHz, T _C = 110°C)	IFRM	2.0	Amps	
Non–Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz)	IFSM	40	Amps	
Storage / Operating Case Temperature	Tstg, T _C	– 55 to +150	°C	
Operating Junction Temperature	Tj	- 55 to +125	°C	
Voltage Rate of Change (Rated V _R , $T_J = 25^{\circ}C$)	dv/dt	10,000	V/μs	
HERMAL CHARACTERISTICS	•		•	
Thermal Resistance — Junction-to-Lead ⁽²⁾ Thermal Resistance — Junction-to-Ambient ⁽²⁾	R _{θJL} R _{θJA}	24 80	°C/W	

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage $^{(1)}$	VF	TJ = 25°C	Tj = 125°C	Volts
	= = 1.0 A) = = 2.0 A)	0.5 0.6	0.425 0.58	
Maximum Instantaneous Reverse Current	IR	TJ = 25°C	Tj = 100°C	mA
(V	(R = 40 V) R = 20 V)	0.4 0.02	10 5.0	

This document contains information on a new product. Specifications and information herein are subject to change without notice.

(1) Pulse Test: Pulse Width \leq 250 µs, Duty Cycle \leq 2.0%.

(2) Mounted with minimum recommended pad size, PC Board FR4.

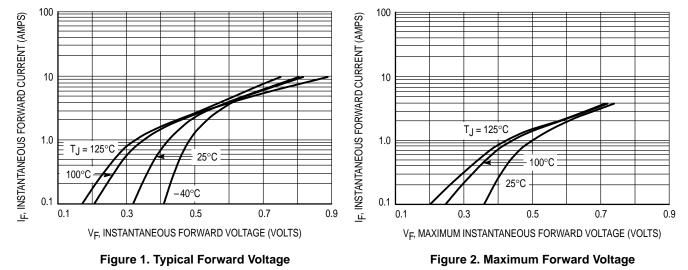


SCHOTTKY BARRIER RECTIFIER 1.0 AMPERES 40 VOLTS





MBRS140LT3



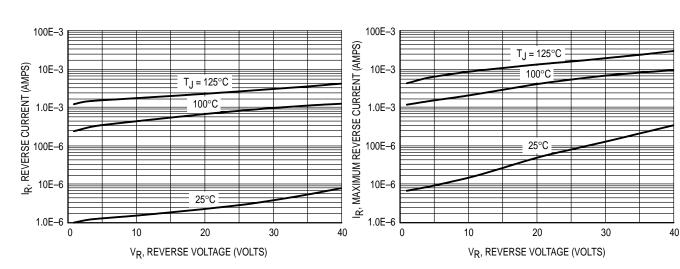
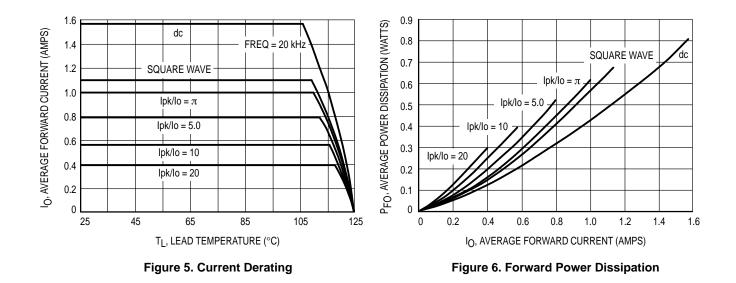




Figure 4. Maximum Reverse Current



MBRS140LT3

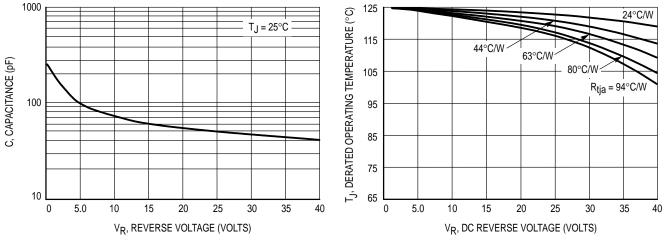


Figure 7. Capacitance

Figure 8. Typical Operating Temperature Derating*

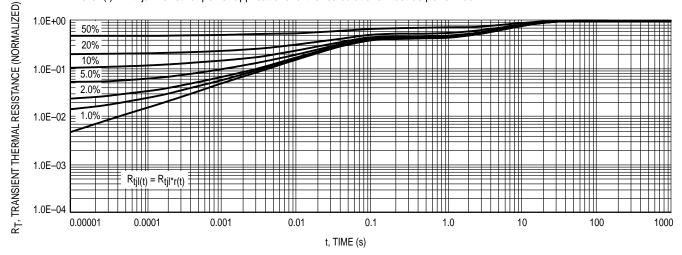
* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T_J therefore must include forward and reverse power effects. The allowable operating T_J may be calculated from the equation: $T_J = T_{Jmax} - r(t)(Pf + Pr)$ where

r(t) = thermal impedance under given conditions,

Pf = forward power dissipation, and

Pr = reverse power dissipation

This graph displays the derated allowable T_J due to reverse bias under DC conditions only and is calculated as $T_J = T_{Jmax} - r(t)Pr$, where r(t) = Rthja. For other power applications further calculations must be performed.





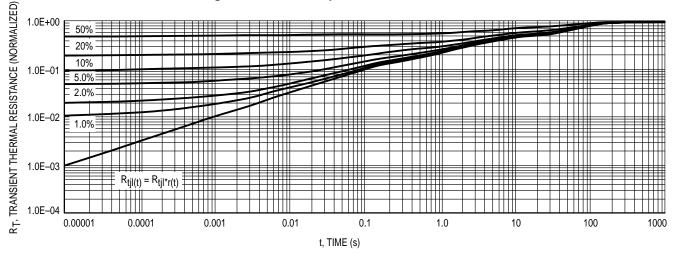
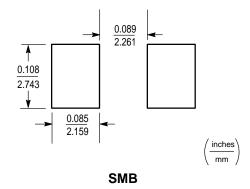
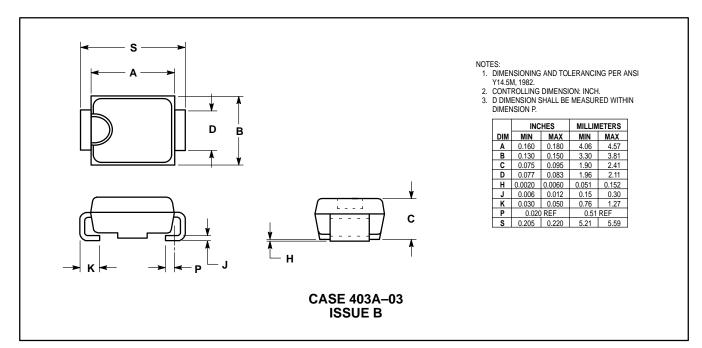


Figure 10. Thermal Response — Junction to Ambient



PACKAGE DIMENSIONS



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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 5405, Denver, Colorado 80217. 303–675–2140 or 1–800–441–2447 JAPAN: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 4–32–1, Nishi–Gotanda, Shinagawa–ku, Tokyo 141, Japan. 81–3–5487–8488

Mfax™: RMFAX0@email.sps.mot.com – TOUCHTONE 602–244–6609 – US & Canada ONLY 1–800–774–1848

INTERNET: http://motorola.com/sps



ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298

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